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## Fabrice Derepas

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Born in 1972.  
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## Experience

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- since 2003 *Commissariat à l'Énergie Atomique* [www.cea.fr](http://www.cea.fr)  
**Head of the software and system engineering department** (Since Jan. 2010)  
In charge of financial balance of the department.  
Managing a team of 80 people.  
**Embedded system program manager** (Nov. 2008- Dec. 2009)  
Define orientations for embedded systems  
Ensure the promotion of technologies, contact with customers,  
Setup transverse projects among several laboratories.  
**Head of the software reliability lab** (Jan. 2006 - Oct 2008)  
Definition of technical goals of the lab,  
Budget execution, work load management,  
Lab marketing, research of financial support,  
Contact with customers.  
**Research Engineer** (2003-2005)  
Specification and design of innovative tools to ensure the safety of hardware architectures.
- 01/2003 - *Edatis Online Marketing* [www.edatis.com](http://www.edatis.com)  
04/2003 **Chief Technical Officer** team of 7 persons.  
Put in place processes for a better production. Direction of development projects.
- 06/2002 - *PriceWaterhouseCoopers Consulting* [www.ibm.com/services](http://www.ibm.com/services)  
12/2002 **Senior Consultant**  
Analysis and specification for the fusion of two bank information systems.
- 11/2001 - *Lynx Software editor* [www.lynxonline.com](http://www.lynxonline.com)  
05/2002 **Chief Technical Officer** team of 4 persons.  
Rebuild the product of the compagny using J2EE technologies.
- 1997-2001 *Nortel Networks* [www.nortel.com](http://www.nortel.com)  
**Web Architect and Designer** (1998-2001)  
Specification and design of a web site using Java and XML.  
creation of reusable components using servlets and JSP.  
Design and maintenance of a web site with 100 000 access monthly.  
**System Architect** (1999-2001)  
New product feature specification and planification.  
Ensure software development coherency in a group of 150 persons.  
Put in place formal methods.  
Strengthen fault tolerance protocols.  
**Software Designer** (1998-1999)  
Software design in a distributed real time environment.  
**Digital Signal Processing Engineer** (1997) - 6 months  
UMTS normalisation.
- 1997 **Research Ingénieur** *Bouygues Telecom* 4 months.  
Specification and design of a model to predict unpaid bills for corporate customers.
- 1996 Internship *AT&T Bell-Labs, USA*, 4 monts.  
Implementation of algorithms used in GSM.

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## Teaching

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- 2004 - 2008 **Desing of software and hardware systems** in second year of Master at École Polytechnique.
- 2004 - 2008 **Advanced Java** in second year of Master at Epita [www.masters.epita.fr](http://www.masters.epita.fr)

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## Education

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- 1998 - 2001 **Phd in computer science** from University Denis Diderot Paris 7.  
Leverage formal methods to check distributed protocols.
- 1996 - 1997 Master in computer science from École Polytechnique.
- 1993 - 1996 Engineer from **École Polytechnique**. Computer science option in first and second year.

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## Languages

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C, C++, Java technologies, PHP, SQL,  
SPARC assembly. FPGA programming. Unix administration.